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INFORMATION DISCLOSURE CITATIONAtty Docket
BUR920000123US1Serial No.
To be assigned

Applicant

Distler et al.

Filing Date
HerewithGroup Art Unit
To be assigned**U.S. PATENT DOCUMENTS**

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
A.W.	AA	3,783,254	01/74	Eichelberger			
A.W.	AB	4,428,060	01/84	Blum			
A.W.	AC	5,377,197	12/94	Patel et al.			
A.W.	AD	5,392,298	02/95	Shinjo			
A.W.	AE	5,544,173	08/96	Meltzer			
A.W.	AF	5,748,497	05/98	Scott et al.			
	AG						
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	AI						
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	AK						

1c760 U.S. PTO
09/768121
01/23/01

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
	AL						Yes No
	AM						Yes No
	AN						Yes No
	AO						Yes No
	AP						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

A.W.	AR		IBM Technical Disclosure Bulletin, "Test Time/Data Volume Reduction Technique" T.W. Sehr, Vol. 24, No. 5, Oct. 1981; pp.2560-2562.
A.W.	AS		IBM Technical Disclosure Bulletin, "Method for Masking Unpredictable Shift Register Latch States During Random Pattern Self-Test", K.T. Kaliszewski; Vol. 36, No. 02, Feb. 1993; pp. 239-241.
	AT		

Examiner

Anthony

Date Considered

5-29-03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 600. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant(s).